

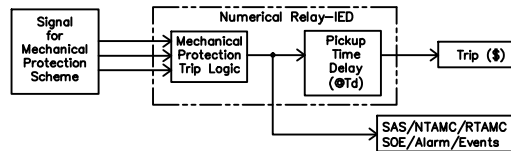
IMPORTANT NOTES:

1. DC SELECTION FOR PROTECTION SYSTEM

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PANEL DC-1	DIFFERENTIAL, HV OC&EF, IV BACKUP IMP, BB PU-A($\geq 400kV$), TC-1
PANEL DC-2	REF, IV OC&EF, HV BACKUP IMP, BB PU-B($\leq 400kV$), TC-2
BUSBAR DC-1	BB CU-A($\geq 400kV$)
BUSBAR DC-2	BB CU-B($\geq 400kV$)
SELECTED DC	CB CLOSE, BOLL STAND ALONE LBS, FOR SINGLE BUSBAR RELAY-CU & PU($\leq 220kV$), VT SELECTION, CT SELECTION

- FOR CENTRALISED BUSBAR PROTECTION 96 TRIP RELAY SHOULD BE PROVIDED.
- IN THE SCHEME, CMR(CONTACT MULTIPLICATION RELAY) MUST NOT BE USED EXCEPT FOR TRIP EXTENSION TO CIRCUIT BREAKER.
- RETRIP SHOULD BE EXTENDED TO TC-1 & TC-2 COIL THROUGH DIRECT CONTACT OF PU-A & PU-B IN DECENTRALISED BUSBAR SCHEME AND THROUGH STANDALONE LBS IN CENTRALISED BUSBAR SCHEME.
- ALL THE TRIPPING CIRCUIT SHOULD BE ISOLATED BY TEST PLUG CONTACT.
- IN CASE OF FUTURE BAY(WITHOUT CT & CB), THE CB LBS BACKTRIP & THE CT CORE HAS TO BE EXTENDED TO BUSBAR WITH FUTURE BAY, ALSO BUSBAR TRIP(FUTURE BAY SIDE) TO BE EXTENDED TO THE CB OF HALF DIA.
- IN CASE OF FUTURE BAY(WITH CT & CB), THE CT CORE HAS TO BE EXTENDED TO BUSBAR WITH FUTURE BAY, IN ADDITION TO THIS, THE BAY LBS BACKTRIP ASSIGNMENT HAS TO BE IMPLEMENTED AS PER COMPLETE DIA SPECIFIC SCHEME. ALSO BUSBAR TRIP(FUTURE BAY SIDE) TO BE EXTENDED TO THE MAIN CB(FUTURE BAY).
- IF DOUBLE BUCHHOLZ RELAYS AVAILABLE, THEN BUCHHOLZ ALARM SHOULD NOT BE EXTENDED TO TRIP.
- PRV OF OLTC & SPR PROTECTION OF ICT(MAIN TANK AS WELL AS OLTC) SHALL BE CONFIGURED AS ALARM ONLY AND SHALL NOT INITIATE ANY TRIPPING.
- MINIMUM BREAKER CLOSING TIME - 200ms, MINIMUM TRIPPING TIME - 100ms.

Transformer Body Protection Logic



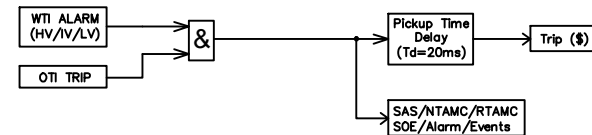
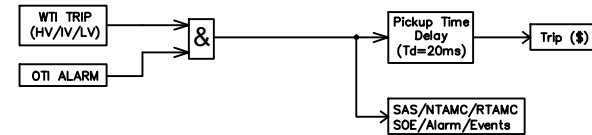
Note:

- ⓈTd = 200ms -> For Buchholz Alarm
- = 200ms -> For Buchholz Trip
- = 20ms -> For Other Body Protection Schemes as applicable

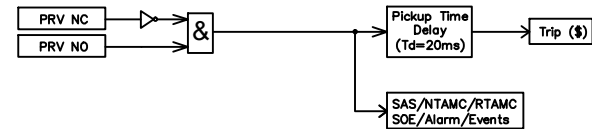
Trip (\$) = Final Mechanical Tripping to Master trip(B6A & B) as per Trip matrix

Note: Final Trip Output need to be configured for Relay Internal Events.

A. WTI/OTI Protection Logic



B. PRV of Main Tank Protection Logic



C. OSR Protection Logic

